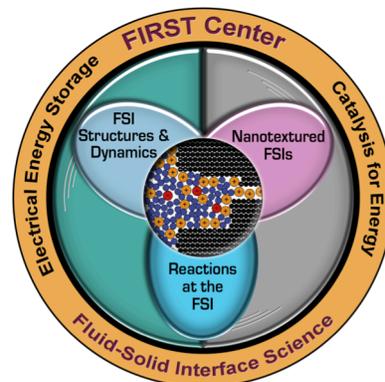


FIRST Center Research Perspective:

Title: Micro-supercapacitors

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Research Summary: We developed miniaturized on-chip supercapacitors, also called micro-supercapacitors, with high power density, rate handling ability and frequency response. We showed that by a rational design of the device architecture and electrode materials, micro-supercapacitors could approach the power density of conventional electrolytic capacitors, while delivering much higher energy density. We also demonstrated that the energy density of micro-supercapacitors could be further increased by using ionic liquids as the electrolyte. Furthermore, we showed that by using a eutectic mixture of ionic liquids as the electrolyte, micro-supercapacitors can be made functional over a wide range of temperatures from $-50\text{ }^{\circ}\text{C}$ to $80\text{ }^{\circ}\text{C}$. Microscale supercapacitor electrodes also enable *in situ* studies of fundamental charge-discharge mechanisms of electrochemical capacitors.

Technical Details:

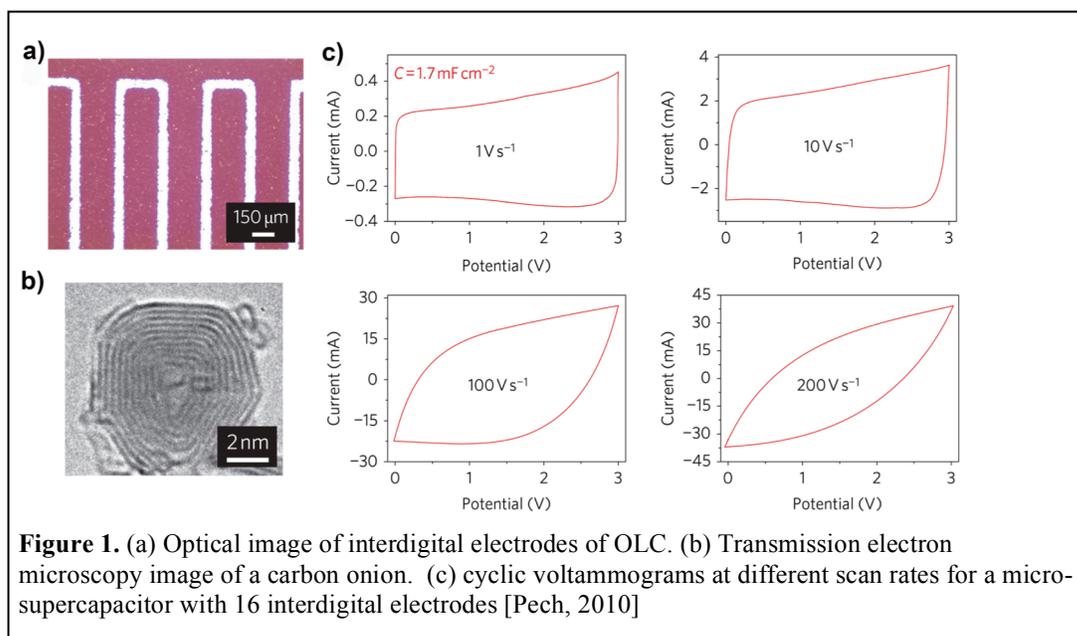


Figure 1. (a) Optical image of interdigital electrodes of OLC. (b) Transmission electron microscopy image of a carbon onion. (c) cyclic voltammograms at different scan rates for a micro-supercapacitor with 16 interdigital electrodes [Pech, 2010]

Building on extensive fundamental studies of onion-like carbon (OLCs)-electrolyte interfaces conducted by FIRST researchers (McDonough, 2011; McDonough, 2013; Anjos, 2013) we developed OLC-based devices. A micro-supercapacitor fabricated by electrophoretic deposition of OLC on the interdigital gold electrodes patterned on a Si wafer is shown in **Figure 1a**. The OLC-based micro-supercapacitors were tested in a typical organic electrolyte (1 M NET_4BF_4

solution in propylene carbonate) and offered an ultra-high power handling capability and could be cycled at very high scan rates (**Figure 1c**), showing capacitive performance at cyclic voltammetry (CV) scan rates as high as 100 Vs^{-1} . Also, Electrochemical Impedance Spectroscopy (EIS) studies showed a resistive-capacitive (RC) time constant of only 26 ms for these micro-supercapacitors. The observed performance is related to properties of electrode materials, binder-free fabrication method and also interdigitated electrode architecture of these microdevices. OLC has an exohedral structure (**Figure 1b**) with no interparticle porosity, which offers a lower specific surface area compared to porous activated and carbide-derived carbon materials. However, the surface of OLC is highly accessible for electrolyte ions resulting in a high rate capability and high power density of OLC based supercapacitor electrodes. Also, the fabrication of the electrodes by electrophoretic deposition eliminates the need to use polymeric binder in the electrodes, which results in their higher conductivity. We showed that the architecture of a micro-supercapacitor also has immense impact on its performance. The small distance between the microelectrode fingers results in the reduction of mean ionic diffusion path between the electrodes. The effect of the size and spacing of the interdigital electrodes on the performance of the device was studied by changing the number of electrode fingers for a constant geometrical surface area of the electrode. For a higher number of the electrode fingers, (i.e. smaller width of the electrodes and smaller spacing between them) a higher specific capacitance and a better rate capability was achieved. The OLC-based micro-supercapacitor showed a maximum power density of 1 kWcm^{-3} , approaching the power density of electrolytic capacitors, and offering orders of magnitude higher energy density at the same time.

We also used a eutectic mixture of ionic liquids (IL) as electrolyte for OLC based micro-supercapacitors and demonstrated a larger working potential and also working temperature range for these microdevices (Huang, 2013). This device was electrochemically characterized by cyclic voltammetry and electrochemical impedance spectroscopy in $[\text{Pip}_{13}\text{FSI}:\text{Py}_{14}\text{FSI}]$ 1:1 ionic liquid electrolyte mixture at different scan rates and different temperatures. At $20 \text{ }^\circ\text{C}$, a micro-supercapacitor operating in a voltage window of 3.7 V and tested at a CV scan rate of 200 mVs^{-1} showed a capacitance of 1.1 mFcm^{-2} per footprint area of the device. The microdevice maintained 76% of its capacitance when tested at $-50 \text{ }^\circ\text{C}$ at the same voltage range and with a scan rate of 10 mVs^{-1} . Therefore, we showed that by using the IL mixture, OLC based micro-supercapacitors can be potentially used in portable electronic devices that are required to work under temperature extremes.

Micro-supercapacitors are relatively new devices with a great potential for applications in microelectronics and self-powered micro/nanosystems. In a recent review paper (Beidaghi, 2013), we have summarized the current state of research and development of micro-supercapacitor technology and identified the key scientific and technological challenges in developing micro-supercapacitors with improved power and energy densities. The performance of micro-supercapacitors is highly dependent on the architecture of the device. Among the possible device architectures for on-chip micro-

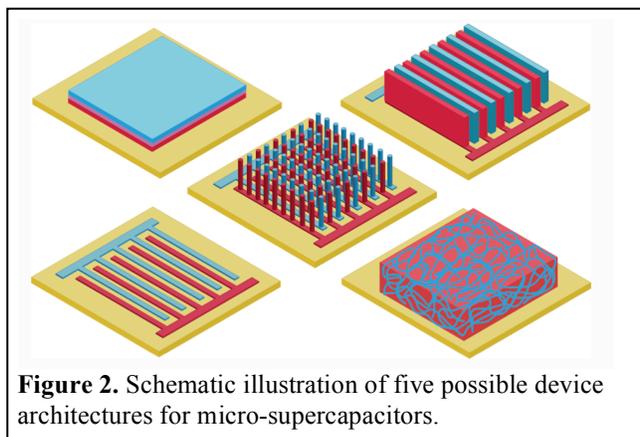


Figure 2. Schematic illustration of five possible device architectures for micro-supercapacitors.

supercapacitors (as shown in **Figure 2**), a three-dimensional design could potentially lead to a much higher energy density.

Significant Impacts on Science and Technology: Our work on design and fabrication of micro-supercapacitors, showed that by designing the device architecture and using appropriate electrode materials and electrolyte, supercapacitors with extremely high power density and frequency response can be developed. Our study shows the promise of supercapacitors for replacing electrolytic capacitors for line-filtering application or powering devices working at extreme temperatures.

Publications and Manuscripts:

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2. Huang, P.; Pech, D.; Lin, R.; McDonough, J.K.; Brunet, M.; Taberna, P.L.; Gogotsi, Y.; Simon P. On-Chip Micro-Supercapacitors for Operation in a Wide Temperature Range, *Electrochem. Commun.* **2013**, *36*, 53-56, [DOI: [10.1016/j.elecom.2013.09.003](https://doi.org/10.1016/j.elecom.2013.09.003)].
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